

Notice of References Cited	Application/Control No. 10/073,830	Applicant(s)/Patent Under Reexamination FOLEA, RICHARD V.	
	Examiner Cynthia Britt	Art Unit 2133	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,279,123	08-2001	Mulrooney, Timothy J.	714/35
	C	US-6,629,282	09-2003	Sugamori et al.	714/734
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	M	US-			

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NON-PATENT DOCUMENTS

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	U	"PATRIOT-A Boundary-Scan Test and Diagnosis System" by Wang et al. Thirty-Seventh IEEE Computer Society International Conference Compcon Spring '92, Digest of Papers 24-28 Feb. 1992 pages 436 - 439 Inspec Accession Number: 4315153
	V	A structured graphical tool for analyzing boundary scan violations Cogswell et al. IBM Corp., Endicott, NY, USA : Test Conference, 2002. Proceedings. International Publication Date: 7-10 Oct. 2002 page(s): 755 - 762 Inspec Accession Number: 7528844
	W	"JTAG Visualizer Makes Boundary Scan Visible Powerful Tool at DFT Assessment and Repair f PCB's" JTAG Technologies Press information release date November 6, 2001.
	X	Corelis ScanPlus Boundary-Scan and programming system product overview pages 7-8 1999-2002 Corelis inc

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

